IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **DeWitt, Jr. et al.** §	Group Art Unit: **2192**
§	
Serial No.: **10/757,197** §	Examiner: **Dam, Tuan Quang**
§	
Filed: **January 14, 2004** §	Attorney Docket No.: **AUS920030556US1**
§	

For: **Method and Apparatus for Optimizing Code Execution Using Annotated Trace Information Having Performance Indicator and Counter Information**
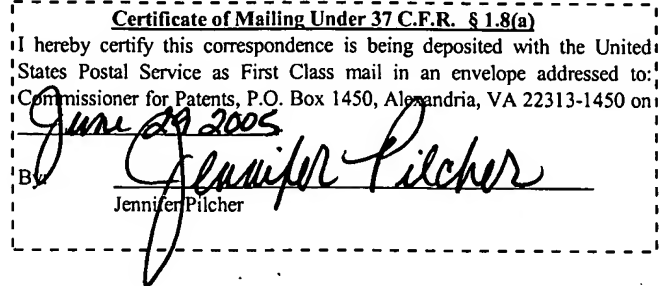
35525
PATENT TRADEMARK OFFICE
CUSTOMER NUMBER

TRANSMITTAL DOCUMENT

Commissioner for Patents
P.O. Box 1450
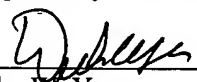Alexandria, VA 22313-1450

Sir:
ENCLOSED HEREWITH:

- Supplemental Information Disclosure Statement;
- Form PTO-1449;
- References AA-AX; and
- Our return postcard.

No fees are believed to be necessary. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to Deposit Account No. 09-0447. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to Deposit Account No. 09-0447.

Respectfully submitted,

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ATTORNEY FOR APPLICANTS

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	§ Group Art Unit: **2192**
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Information	§

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above entitled application, pursuant to 37 C.F.R. 1.97.

Please charge any fees necessary for prosecution of the present application to Deposit Account No. 09-0447. If any extension of time is required, such extension is hereby requested. Please charge any additional required fee for extension of time to Deposit Account No. 09-0447.

In accordance with 37 C.F.R. 1.97(h), the filing of this Supplemental Information Disclosure Statement shall not constitute an admission that any information cited therein is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56(b). In the interest of full and complete disclosure to the Office, some or all of the art cited herein may not be considered by Applicant(s) or the Undersigned to be material under the new standards of materiality defined in 37 C.F.R. 1.56(b), enacted March 16, 1992, but may be material under the old standard of materiality defined in 37 C.F.R. 1.56(a), last amended on November 28, 1988, or may merely be technical background which may be of interest to

the Examiner. In accordance with 37 C.F.R. 1.97(g), the filing of this Supplemental Information Disclosure Statement shall not be construed to mean that a search has been made.

This Supplemental Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) within three months of the filing date of the application, or before the mailing date of a first office action on the merits. No fee is required.

Date: _6/2/05_

Respectfully submitted,

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Form PTO-1449			ATTORNEY DOCKET NO. AUS920030556US1		SERIAL NO. 10/757,197
LIST OF PRIOR ART CITED BY APPLICANT *(Use several sheets if necessary)*					
			APPLICANT DeWitt, Jr. et al.	.	
			FILING DATE January 14, 2004		GROUP ART UNIT 2192

RELATED PATENT APPLICATIONS

EXAMINER INITIAL		APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
	AA	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
	AB	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
	AC	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
	AD	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
	AE	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
	AF	10/675,751 / AUS920030482US1	DeWitt, Jr. et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
	AG	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
	AH	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
	AI	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions	Sep. 30, 2003
	AJ	10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003
	AK	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003
	AL	10/757,250 / AUS920030488US1	Levine et al.	Method and Apparatus for Maintaining Performance Monitoring Structures in a Page Table for use in Monitoring Performance of a Computer Program	Jan. 14, 2004
	AM	10/757,186 / AUS920030540US1	DeWitt, Jr. et al.	Method and Apparatus for Qualifying Collection of Performance Monitoring Events by Types of Interrupt when Interrupt Occurs	Jan. 14, 2004
	AN	10/757,198 / AUS920030541US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Interrupts by Type	Jan. 14, 2004
	AO	10/757,192 / AUS920030543US1	DeWitt, Jr. et al.	Method and Apparatus for Providing Pre and Post Handlers for Recording Events	Jan. 14, 2004
	AP	10/757,248 / AUS920030544US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses to Identify Hot Spots	Jan. 14, 2004
	AQ	10/757,256 / AUS920030545US1	DeWitt, Jr. et al.	Method and Apparatus for Autonomic Detection of Cache "Chase Tail" Conditions and Storage of Instructions/Data in "Chase Tail" Data Structure	Jan. 14, 2004
	AR	10/757,269 / AUS920030549US1	DeWitt, Jr. et al.	Method and Apparatus for Autonomically Initiating Measurement of Secondary Metrics Based on Hardware Counter Values for Primary Metrics	Jan. 14, 2004
	AS	10/757,237 / AUS920030550US1	DeWitt, Jr. et al.	Autonomic Method and Apparatus for Counting Branch Instructions to Improve Branch Predictions	Jan. 14, 2004
	AT	10/757,171 / AUS920030551US1	DeWitt, Jr. et al.	Autonomic Method and Apparatus for Hardware Assist for Patching Code	Jan. 14, 2004
	AU	10/757,156 / AUS920030552US1	DeWitt, Jr. et al.	Autonomic Method and Apparatus for Local Program Code Reorganization Using Branch Count per Instruction Hardware	Jan. 14, 2004
	AV	10/757,212 / AUS920030553US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Based on Arithmetic Combinations of Performance Counter Values	Jan. 14, 2004
	AW	10/757,249 / AUS920030554US1	DeWitt, Jr. et al.	Method and Apparatus for Identifying False Cache Line Sharing	Jan. 14, 2004
	AX	10/757,227 / AUS920030555US1	DeWitt, Jr. et al.	Method and Apparatus for Autonomically Moving Cache Entries to Dedicated Storage when False Cache Line Sharing is Detected	Jan. 14, 2004

DATE CONSIDERED	EXAMINER

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.